



PTO/SB/08A (08-03)

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

**Complete if Known**

Application Number	10/804,346
Filing Date	19 March 2004
First Named Inventor	Philip Neaves
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	DB001095-000

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 6,570,437 B2	05-27-2003	Park et al.	
		US- 6,531,857 B2	03-11-2003	Ju	
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				

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SHAWN RILEY  
PRIMARY EXAMINER

Jun 2005  
Mail Date  
30 June 2004

PTO/SB/08B (08-03)

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Substitute for form 1449/PTO		<b>Complete if Known</b>			
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		Application Number	10/804,346		
		Filing Date	19 March 2004		
		First Named Inventor	Philip Neaves		
		Art Unit	Not Yet Assigned		
		Examiner Name	Not Yet Assigned		
Sheet	2	of	2	Attorney Docket Number	DB001095-000

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
SR		HIRONORI BANBA et al., A CMOS Bandgap Reference Circuit with Sub-1-V Operation, IEEE Journal of Solid-State Circuits, May 1999, 670-674, Vol. 34, No. 5,	
SR		K. LASANEN, et al., Design of a 1-V Low Power CMOS Bandgap Reference Based on Resistive Subdivision available at website www.ee.oulu.fi/~Kimi/III-564.pdf on 8/11/03, 4 pgs.	
SR		F. MALOBERTI, Curvature Compensated Bicmos Bandgap with 1 V Supply Voltage available at website www.ewh.ieee.org/soc/cas/dallas/documents/MalobertiBGAP.pdf on 8/11/03, 29 pgs.	
SR		M. WALTARI, et al., Reference Voltage Driver for Low-Voltage CMOS A/D Converters available at website www.ecdl.hut.fi/~mwa/publications/2000/ICECS00_1.pdf on 8/11/03, 4 pgs.	

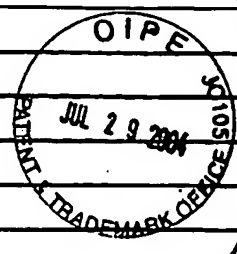
Examiner Signature	Riley	Date Considered	Jun 05
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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	10/804,346
		Filing Date	19 March 2004
		First Named Inventor	Philip Neaves
		Art Unit	2838
		Examiner Name	Not Yet Assigned
Attorney Docket Number	DB001095-000		
Sheet	1	of	1



NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		U.K. Patent Office Search Report dated 1 July 2004, in corresponding U.K. Patent Application GB0320865.9, 1 pg.	

Examiner Signature	<i>Riley</i>	Date Considered	Jun 05
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